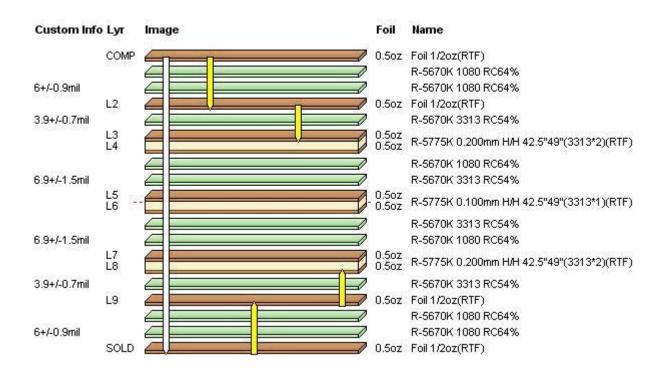
COPPER PLATE HOLES MINIMUM .025 AVG, .020 MIN.. HOLES MAY NOT BE PLUGGED This PN required 10% impedance, 8mil laser drill, POFV, stacked via, laser blind via drill thru 6mil dielectric and plug and plating flat via

Pack with colorless transparent bubble film ,25 PCS/ bag, put desiccant in flank, put humidity indicator card on top side

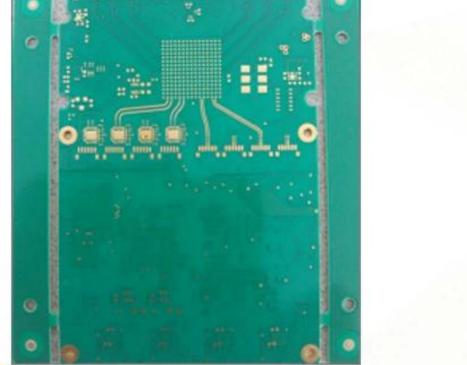
Layer structure







SGS



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<u>Quick turn pcb Printed circuit board, Pcb design in china, HDI pcb Printed circuit board</u>